

**METHOD FOR IMPLEMENTING DYNAMIC VIRTUAL LANE BUFFER  
RECONFIGURATION**

**Abstract of the Disclosure**

A method, apparatus and computer program product are provided for  
5 implementing dynamic Virtual Lane buffer reconfiguration in a channel  
adapter. A first register is provided for communicating an adapter buffer size  
and allocation capability for the channel adapter. At least one second  
register is provided for communicating a current port buffer size and one  
second register is associated with each physical port of the channel adapter.  
10 A plurality of third registers is provided for communicating a current VL buffer  
size, and one third register is associated with each VL of each physical port  
of the channel adapter. The second register is used for receiving change  
requests for adjusting the current port buffer size for an associated physical  
port. The third register is used for receiving change requests for adjusting  
15 the current VL buffer size for an associated VL.